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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sebastian Feroussat
Serial No: 09/919,949 475
Confirmation No: 4927
Filed: July 30, 2001
For: CARRY SAVE ADDERS

Examiner: T.V. Mai
Art Unit: 2193

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to BOX RCE Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the 23rd day of December, 2005.


Eileen MacKenzie

BOX RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114. No fee or certification is required.

PART II: Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

PART III: Remarks

A copy of any document cited anywhere in the Information Disclosure Statement is enclosed unless otherwise indicated. It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his or her own conclusion regarding the relevance of the cited information.

Serial No.: 09/919,949
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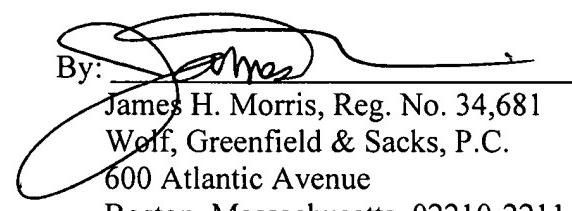
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An early and favorable action is hereby requested.

Respectfully submitted,

Sebastien Ferroussat, *Applicant*

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Date: December 23, 2005

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DEC 28 2005

FORM PTO-1449/A and B (Modified)		APPLICATION NO.: 09/919,949 490	ATTY. DOCKET NO.: S1022.80718US00
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE: July 30, 2001	CONFIRMATION NO.: 4927
		APPLICANT: Sebastien Ferroussat	
Sheet	of	GROUP ART UNIT: 2193	EXAMINER: T.V. Mai

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
		4,604,723		James L. .Burrows	08/05/86

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		European Search Report from European Patent Application 00410092.1 filed August 1, 2000	
		Mayur Mehta et al. "High-Speed Multiplier Design Using Multi-Input Counter And Compressor Circuits" Proceedings of the Symposium on Computer Arithmetic, US, Los Alamitos, IEEE Comp. Soc. Press, vol. SYMP. 10, June 26, 1991, pages 43-50, XP000222157	
		Norio Ohkubo et al. "A 4.4 NS CMOS 54 X 54-B Multiplier Using Pass-Transistor Multiplexer" IEEE Journal of Solid-State Circuits, US, IEEE Inc., New York, vol. 30, no. 3, March 1, 1995, pages 251-256, XP000502811	

EXAMINER

DATE CONSIDERED

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered.
 Include copy of this form with next communication to applicant.